Department of Electrical Engineering & Computer Science
Teaching Assistant Application
Winter 2015

Please return completed form to the EECS Graduate Office
2201 Engineering Hall
emails are accepted – eecsgrad@uci.edu

Application deadline is: **December 5th, 2014**

Name ___________________________ Student ID Number _________________
(Please Print – First Name then Family Name)

E-mail Address __________________________ GPA (as of Fall 2014) _____

(GPA must be at least 3.1, if you are an MS student, or 3.5, if you are a PhD student).

Degree Program:  MS ___  Ph.D. ___

Area of Research__________  PhD Graduate Advisor __________

Are you receiving any fellowship or a GSR for Academic Year 2014-2015? If so, please indicate name of fellowship and dollar amount, and/or name of advisor and salary percentage:

Name of Award/GSR Advisor ______________________________ Amount of Award/Salary $__________

**Requesting CPT for the quarter? _____** If so, how many hours/week will you be working? ______

Have you previously served as a TA? Please list the name of the class and the quarter of your appointment ________________________________

Please give your employee number ____________

REQUIREMENT: Please remember that you must have taken the courses you are applying for or its equivalent. If you have taken a course that is the equivalent, please attach a course syllabus

**Foreign students or permanent resident students who did not have four years of high school education in the USA,** must have taken and passed the TSE, SPEAK or TOEP **before** filling out an application. Passing scores are at least a 5 on the TOEP, a 50 on the TSE and 5 on the Speak.

If you have not already been a TA at UCI, please attach a copy of your score.

Please note: Submission of this form does not guarantee you a TA position.

Please keep in mind that not all classes will be assigned a TA. Some classes may have already been assigned a teaching assistant by an advisor.
Please indicate your 1st, 2nd & 3rd choices on the following list of classes:

<table>
<thead>
<tr>
<th>EECS</th>
<th>Course Description</th>
<th>Instructor</th>
<th>EECS</th>
<th>Course Description</th>
<th>Instructor</th>
</tr>
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<tbody>
<tr>
<td>12</td>
<td>INTRO PROGRAMMING</td>
<td>Lin, K</td>
<td>150</td>
<td>CONT-TM SIGS &amp; SYSTM</td>
<td>Anandkumar</td>
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<tr>
<td>22L</td>
<td>SOFTWARE ENGR IN C</td>
<td>Doemer</td>
<td>152B</td>
<td>DSP DESIGN &amp; LAB</td>
<td>Swindlehurst</td>
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<tr>
<td>31L</td>
<td>INTRO DIG LOGIC LAB</td>
<td></td>
<td>144</td>
<td>ANTENNA DESIGN</td>
<td>Capolino</td>
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<tr>
<td>55</td>
<td>ENGR PROBABILITY</td>
<td>Jafarkhani</td>
<td>166A</td>
<td>I&amp;P ELECTRONICS</td>
<td>Smedley</td>
</tr>
<tr>
<td>70A</td>
<td>NETWORK ANALYS I</td>
<td>De Flaviis</td>
<td>170B</td>
<td>ELECTRONICS</td>
<td>Lee, H</td>
</tr>
<tr>
<td>70LA</td>
<td>NET ANALYS I LAB</td>
<td>De Flaviis</td>
<td>170LB</td>
<td>ELECTRONICS II</td>
<td>Lee, H</td>
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<tr>
<td>101</td>
<td>INTRO TO MACH VISS</td>
<td>Healey</td>
<td>170E</td>
<td>ANALOG/COMM IC DES</td>
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<tr>
<td>112L</td>
<td>ORG DIGITL COMP LAB</td>
<td>Eltawil</td>
<td>174</td>
<td>SEMICONDR DEVICES</td>
<td>Lee, H</td>
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<td>118</td>
<td>KNOW ENGR &amp; SW ENGR</td>
<td>Sheu, P</td>
<td>180A</td>
<td>ENGR ELECTROMAG I</td>
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<tr>
<td>141A</td>
<td>COMMUNC TN SYSTMS I</td>
<td>Eltawil</td>
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<tr>
<td>159B</td>
<td>SENIOR PROJECT II</td>
<td>Chou, P</td>
<td>CSE</td>
<td>181B SENIOR DESIGN PROJ</td>
<td>Chou, P</td>
</tr>
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</table>

PLEASE READ THE FOLLOWING TA RULES AND REGULATIONS:

You are not allowed to have a “U”, “NP”, or failing grades for the past 3 quarters. Your eligibility for employment is contingent upon you satisfying the following standard appointment criteria: (a) enrollment of a minimum of 12 units including at least 6 units of directed research course each quarter; (b) a GPA of at least 3.1 if you are a Master’s Degree student, or a 3.5 if you are a PhD student during each quarter and at least at the A- level for the research course; (c) no more than two Incomplete’s (I) and one No Report (NR) grades during the three preceding quarters, and at least a B+ for the course you are interested in TAing.

We reserve the right to terminate your appointment at any time for unsatisfactory performance, unsatisfactory progress toward your degree objective or academic dishonesty.

IMPORTANT NOTE: TA appointments depend solely on the class enrollment. Because of this reason, some appointments will not be made before the September 15th tuition and fee payment deadline. Even though you have applied for a TA position, it is YOUR responsibility to pay your fees and tuition on time. If you are chosen as a TA, you will be reimbursed for your fees. The TAship does not pay the non-resident tuition. You will be responsible for any and all late fees.

If you are chosen as a TA, you may not receive any other financial support, other than non-resident tuition and any un-reimbursed portion of your fees that are paid by your advisor, totaling more than 50% time (220 hours assigned workload).

Application deadline is December 10th, 2014.

Please be aware that TA appointments available in Winter, 2015 are very limited.

☐ Please check the box if you have read and understand your responsibilities.